G	Sektion	SECTION G — PHYSICS
G06	Klasse	COMPUTING; CALCULATING; COUNTING (score computers for games A63B 71/06, A63D 15/20, A63F 1/18; combinations of writing implements with computing devices B43K 29/08)
G06F	Unterklasse	ELECTRIC DIGITAL DATA PROCESSING (computers in which a part of the computation is effected hydraulically or pneumatically G06D, optically G06E; computer systems based on specific computational models G06N; impedance networks using digital techniques H03H)
G06F 1/00	Hauptgruppe	Details not covered by groups G06F 3/00-G06F 13/00 and G06F 21/00 (architectures of general purpose stored programme computers G06F 15/76) [1, 2006.01]
G06F 1/02	1-Punkt Untergruppe	. Digital function generators
G06F 1/025	2-Punkt Untergruppe	for functions having two-valued amplitude, e.g. Walsh functions [5]
G06F 1/03	2-Punkt Untergruppe	working, at least partly, by table look-up (G06F 1/025 takes precedence) [5]
G06F 1/035	3-Punkt Untergruppe	Reduction of table size [5]
G06F 1/04	1-Punkt Untergruppe	. Generating or distributing clock signals or signals derived directly therefrom
G06F 1/06	2-Punkt Untergruppe	Clock generators producing several clock signals [5]
G06F 1/08	2-Punkt Untergruppe	Clock generators with changeable or programmable clock frequency [5]
G06F 1/10	2-Punkt Untergruppe	Distribution of clock signals [5]
G06F 1/12	2-Punkt Untergruppe	Synchronisation of different clock signals [5]
G06F 1/14	2-Punkt Untergruppe	Time supervision arrangements, e.g. real time clock [5]
G06F 1/16	1-Punkt Untergruppe	. Constructional details or arrangements (instrument details G12B) [5]
G06F 1/18	2-Punkt Untergruppe	Packaging or power distribution [5]
G06F 1/20	2-Punkt Untergruppe	Cooling means [5]
G06F 1/22	1-Punkt Untergruppe	. Means for limiting or controlling the pin/gate ratio [5]
G06F 1/24	1-Punkt Untergruppe	. Resetting means (micro-programme loading G06F 9/24; restoration from data faults G06F 11/00) [5]
G06F 1/26	1-Punkt Untergruppe	. Power supply means, e.g. regulation thereof (for memories G11C) [5]
G06F 1/28	2-Punkt Untergruppe	Supervision thereof, e.g. detecting power-supply failure by out of limits supervision [5]
G06F 1/30	2-Punkt Untergruppe	Means for acting in the event of power-supply failure or interruption, e.g. power-supply fluctuations (for resetting only G06F 1/24; involving the processing of data-words G06F 11/00) [5]
G06F 1/32	2-Punkt Untergruppe	Means for saving power [5]
G06F 3/00	Hauptgruppe	Input arrangements for transferring data to be processed into a form capable of being handled by the computer; Output arrangements for transferring data from processing unit to output unit, e.g. interface arrangements (typewriters B41J; conversion of physical variables F15B 5/00, G01; image acquisition G06T 1/00, G06T 9/00; coding, decoding or code conversion, in general H03M; transmission of digital information H04L) [4]
G06F 3/01	1-Punkt Untergruppe	. Input arrangements or combined input and output arrangements for interaction between user and computer (G06F 3/16 takes precedence) [2006.01]

Symbol	Тур	Titel
G06F 3/02	2-Punkt Untergruppe	Input arrangements using manually operated switches, e.g. using keyboards or dials ( keyboard switches
		per se H01H 13/70; electronic switches characterised by the way in which the control signals are
		generated H03K 17/94 ) [3, 2006.01]
G06F 3/023	3-Punkt Untergruppe	Arrangements for converting discrete items of information into a coded form, e.g. arrangements for interpreting keyboard generated codes as alphanumeric codes, operand codes or instruction codes ( coding in connection with keyboards or like devices in general H03M 11/00) [3, 2006.01]
G06F 3/027	4-Punkt Untergruppe	for insertion of the decimal point [3, 2006.01]
G06F 3/03	2-Punkt Untergruppe	Arrangements for converting the position or the displacement of a member into a coded form [3, 2006.01]
G06F 3/033	3-Punkt Untergruppe	Pointing devices displaced or positioned by the user, e.g. mice, trackballs, pens or joysticks; Accessories therefor [3, 2006.01]
G06F 3/037	4-Punkt Untergruppe	using the raster scan of a cathode-ray tube (CRT) for detecting the position of the member, e.g. light pens cooperating with CRT monitors [3, 2006.01]
G06F 3/038	4-Punkt Untergruppe	Control and interface arrangements therefor, e.g. drivers or device-embedded control circuitry [2006.01]
G06F 3/039	4-Punkt Untergruppe	Accessories therefor, e.g. mouse pads (furniture aspects A47B 21/00) [2006.01]
G06F 3/041	3-Punkt Untergruppe	Digitisers, e.g. for touch screens or touch pads, characterised by the transducing means [2006.01]
G06F 3/042	4-Punkt Untergruppe	by opto-electronic means [2006.01]
G06F 3/043	4-Punkt Untergruppe	using propagating acoustic waves [2006.01]
G06F 3/044	4-Punkt Untergruppe	by capacitive means [2006.01]
G06F 3/045	4-Punkt Untergruppe	using resistive elements, e.g. a single continuous surface or two parallel surfaces put in contact [2006.01]
G06F 3/046	4-Punkt Untergruppe	by electromagnetic means [2006.01]
G06F 3/047	4-Punkt Untergruppe	using sets of wires, e.g. crossed wires [2006.01]
G06F 3/048	2-Punkt Untergruppe	Interaction techniques for graphical user interfaces, e.g. interaction with windows, icons or menus [2006.01]
G06F 3/05	1-Punkt Untergruppe	. Digital input using the sampling of an analogue quantity at regular intervals of time (sample-and-hold arrangements G11C 27/02; sampling per seH03K 17/00; analogue/digital conversion, in general H03M 1/00)
G06F 3/06	1-Punkt Untergruppe	. Digital input from, or digital output to, record carriers
G06F 3/08	2-Punkt Untergruppe	from or to individual record carriers, e.g. punched card
G06F 3/09	1-Punkt Untergruppe	. Digital output to typewriters [3]
G06F 3/12	1-Punkt Untergruppe	. Digital output to print unit (digital output to typewriter G06F 3/09; arrangements for producing a permanent visual presentation of the output data using printers G06K 15/02)
G06F 3/13	1-Punkt Untergruppe	. Digital output to plotter (arrangements for producing a permanent visual presentation of the output data using plotters G06K 15/22) [3]
G06F 3/14	1-Punkt Untergruppe	. Digital output to display device (arrangements for producing a permanent visual presentation of the output data G06K 15/00; control of display in general G09G) $$
G06F 3/147	2-Punkt Untergruppe	using display panels [3]

Symbol	Тур	Titel
G06F 3/153	2-Punkt Untergruppe	using cathode-ray tubes [3]
G06F 3/16	1-Punkt Untergruppe	. Sound input; Sound output (conversion of speech into digital information or <u>vice versa</u> G10L)
G06F 3/18	1-Punkt Untergruppe	. Digital input from automatic curve follower (automatic curve followers per seG06K 11/02) [3]
G06F 5/00	Hauptgruppe	Methods or arrangements for data conversion without changing the order or content of the data handled (coding, decoding or code conversion, in general H03M) [4]
G06F 5/01	1-Punkt Untergruppe	. for shifting, e.g. justifying, scaling, normalising [5]
G06F 5/06	1-Punkt Untergruppe	. for changing the speed of data flow, i.e. speed regularising
G06F 5/08	2-Punkt Untergruppe	having a sequence of storage locations, the intermediate ones not being accessible for either enqueue or dequeue operations, e.g. using a shift register [2006.01]
G06F 5/10	2-Punkt Untergruppe	having a sequence of storage locations each being individually accessible for both enqueue and dequeue operations, e.g. using random access memory [2006.01]
G06F 5/12	3-Punkt Untergruppe	Means for monitoring the fill level; Means for resolving contention, i.e. conflicts between simultaneous enqueue and dequeue operations [2006.01]
G06F 5/14	4-Punkt Untergruppe	for overflow or underflow handling, e.g. full or empty flags [2006.01]
G06F 5/16	2-Punkt Untergruppe	Multiplexed systems, i.e. using two or more similar devices which are alternately accessed for enqueue and dequeue operations, e.g. ping-pong buffers [2006.01]
G06F 7/00	Hauptgruppe	Methods or arrangements for processing data by operating upon the order or content of the data handled (logic circuits $H03K\ 19/00)$
G06F 7/02	1-Punkt Untergruppe	. Comparing digital values (G06F 7/06, G06F 7/38 take precedence; information retrieval G06F 17/30; comparing pulses H03K 5/22)
G06F 7/04	2-Punkt Untergruppe	Identity comparison, i.e. for like or unlike values
G06F 7/06	1-Punkt Untergruppe	. Arrangements for sorting, selecting, merging, or comparing data on individual record carriers (sorting of postal letters B07C; conveying record carriers from one station to another G06K 13/02)
G06F 7/08	2-Punkt Untergruppe	Sorting, i.e. grouping record carriers in numerical or other ordered sequence according to the classification of at least some of the information they carry (by merging two or more sets of carriers in ordered sequence G06F 7/16)
G06F 7/10	2-Punkt Untergruppe	Selecting, i.e. obtaining data of one kind from those record carriers which are identifiable by data of a second kind from a mass of ordered or randomly-distributed record carriers
G06F 7/12	3-Punkt Untergruppe	with provision for printing-out a list of selected items
G06F 7/14	2-Punkt Untergruppe	Merging, i.e. combining at least two sets of record carriers each arranged in the same ordered sequence to produce a single set having the same ordered sequence
G06F 7/16	3-Punkt Untergruppe	Combined merging and sorting
G06F 7/20	2-Punkt Untergruppe	Comparing separate sets of record carriers arranged in the same sequence to determine whether at least some of the data in one set is identical with that in the other set or sets
G06F 7/22	1-Punkt Untergruppe	. Arrangements for sorting or merging computer data on continuous record carriers, e.g. tape, drum, disc
G06F 7/24	2-Punkt Untergruppe	Sorting, i.e. extracting data from one or more carriers, re-arranging the data in numerical or other ordered sequence, and re-recording the sorted data on the original carrier or on a different carrier or set of carriers (G06F 7/36 takes precedence)
G06F 7/26	3-Punkt Untergruppe	the sorted data being recorded on the original record carrier within the same space in which the data had been recorded prior to their sorting, without using intermediate storage

Symbol	Тур	Titel	200. 17,11
G06F 7/32	2-Punkt Untergruppe	Merging, i.e. combining data contained in ordered sequence on at least two r single carrier or set of carriers having all the original data in the ordered sequence)	·
G06F 7/36	2-Punkt Untergruppe	Combined merging and sorting	
G06F 7/38	1-Punkt Untergruppe	. Methods or arrangements for performing computations using exclusively denote representation, e.g. using binary, ternary, decimal representation [3]	ominational number
G06F 7/40	2-Punkt Untergruppe	using contact-making devices, e.g. electromagnetic relay (G06F 7/46 takes p	recedence)
G06F 7/42	3-Punkt Untergruppe	Adding; Subtracting	
G06F 7/44	3-Punkt Untergruppe	Multiplying; Dividing	
G06F 7/46	2-Punkt Untergruppe	using electromechanical counter-type accumulators	
G06F 7/48	2-Punkt Untergruppe	using non-contact-making devices, e.g. tube, solid state device; using unspec	cified devices [3]
G06F 7/483	3-Punkt Untergruppe	Computations with numbers represented by a non-linear combination of d rational numbers, logarithmic number system, floating-point numbers (cc point codes H03M 7/24) [2006.01]	=
G06F 7/485	4-Punkt Untergruppe	Adding; Subtracting [2006.01]	
G06F 7/487	4-Punkt Untergruppe	Multiplying; Dividing [2006.01]	
G06F 7/49	3-Punkt Untergruppe	Computations with a radix, other than binary, 8, 16 or decimal, e.g. ternary, radices, mixed radix [3]	, negative or imaginary
G06F 7/491	3-Punkt Untergruppe	Computations with decimal numbers [2006.01]	
G06F 7/492	4-Punkt Untergruppe	using a binary weighted representation within each denomination [2006	.01]
G06F 7/493	5-Punkt Untergruppe	the representation being the natural binary coded representation, i.e. 8	421-code [2006.01]
G06F 7/494	6-Punkt Untergruppe	Adding; Subtracting [2006.01]	
G06F 7/495	7-Punkt Untergruppe	in digit-serial fashion, i.e. having a single digit-handling circuit treating each other [2006.01]	ng all denominations after
G06F 7/496	6-Punkt Untergruppe	Multiplying; Dividing [2006.01]	
G06F 7/498	4-Punkt Untergruppe	using counter-type accumulators [2006.01]	
G06F 7/499	3-Punkt Untergruppe	Denomination or exception handling, e.g. rounding, overflow [2006.01]	
G06F 7/50	3-Punkt Untergruppe	Adding; Subtracting (G06F 7/483-G06F 7/491, G06F 7/544-G06F 7/556 2006.01]	take precedence ) [3,
G06F 7/501	4-Punkt Untergruppe	Half or full adders, i.e. basic adder cells for one denomination (EXCLUSIN [2006.01]	VE-OR circuits H03K 19/21)
G06F 7/502	5-Punkt Untergruppe	Half adders; Full adders consisting of two cascaded half adders [2006.	01]
G06F 7/503	5-Punkt Untergruppe	using carry switching, i.e. the incoming carry being connected directly, carry output under control of a carry propagate signal [2006.01]	or only via an inverter, to the
G06F 7/504	4-Punkt Untergruppe	in bit-serial fashion, i.e. having a single digit-handling circuit treating all of other [2006.01]	denominations after each
G06F 7/505	4-Punkt Untergruppe	in bit-parallel fashion, i.e. having a different digit-handling circuit for each adders G06F 7/501 ) [2006.01]	h denomination ( half or full

Symbol	Тур	Titel
G06F 7/506	5-Punkt Untergruppe	with simultaneous carry generation for, or propagation over, two or more stages [2006.01]
G06F 7/507	6-Punkt Untergruppe	using selection between two conditionally calculated carry or sum values [2006.01]
G06F 7/508	6-Punkt Untergruppe	using carry look-ahead circuits [2006.01]
G06F 7/509	5-Punkt Untergruppe	for multiple operands, e.g. digital integrators [2006.01]
G06F 7/52	3-Punkt Untergruppe	Multiplying; Dividing (G06F 7/483-G06F 7/491, G06F 7/544-G06F 7/556 take precedence) [3, 2006.01]
G06F 7/523	4-Punkt Untergruppe	Multiplying only [2006.01]
G06F 7/525	5-Punkt Untergruppe	in serial-serial fashion, i.e. both operands being entered serially (G06F 7/533 takes precedence) [2006.01]
G06F 7/527	5-Punkt Untergruppe	in serial-parallel fashion, i.e. one operand being entered serially and the other in parallel (G06F 7/533 takes precedence) [2006.01]
G06F 7/53	5-Punkt Untergruppe	in parallel-parallel fashion, i.e. both operands being entered in parallel (G06F 7/533 takes precedence) [2006.01]
G06F 7/533	5-Punkt Untergruppe	Reduction of the number of iteration steps or stages, e.g. using the Booth algorithm, log-sum, odd- even [2006.01]
G06F 7/535	4-Punkt Untergruppe	Dividing only [2006.01]
G06F 7/537	5-Punkt Untergruppe	Reduction of the number of iteration steps or stages, e.g. using the Sweeny-Robertson-Tocher (SRT) algorithm [2006.01]
G06F 7/54	Gelöscht	(transferred to G06F 7/52-G06F 7/537)
G06F 7/544	3-Punkt Untergruppe	for evaluating functions by calculation (with a look-up table G06F 1/02) [3]
G06F 7/548	4-Punkt Untergruppe	Trigonometric functions; Co-ordinate transformations [3]
G06F 7/552	4-Punkt Untergruppe	Powers or roots [3]
G06F 7/556	4-Punkt Untergruppe	Logarithmic or exponential functions [3]
G06F 7/57	3-Punkt Untergruppe	Arithmetic logic units (ALU), i.e. arrangements or devices for performing two or more of the operations covered by groups G06F 7/483-G06F 7/556 or for performing logical operations (instruction execution G06F 9/30) [2006.01]
G06F 7/575	4-Punkt Untergruppe	Basic arithmetic logic units, i.e. devices selectable to perform either addition, subtraction or one of several logical operations, using, at least partially, the same circuitry [2006.01]
G06F 7/58	1-Punkt Untergruppe	. Random or pseudo-random number generators [3]
G06F 7/60	1-Punkt Untergruppe	. Methods or arrangements for performing computations using a digital non-denominational number representation, i.e. number representation without radix; Computing devices using combinations of denominational and non-denominational quantity representations [3]
G06F 7/62	2-Punkt Untergruppe	Performing operations exclusively by counting total number of pulses [3]
G06F 7/64	2-Punkt Untergruppe	Digital differential analysers, i.e. computing devices for differentiation, integration or solving differential or integral equations, using pulses representing increments; Other incremental computing devices for solving difference equations (G06F 7/70 takes precedence; differential analysers using hybrid computing techniques G06J 1/02) [3]
G06F 7/66	3-Punkt Untergruppe	wherein pulses represent unitary increments only [3]
G06F 7/68	2-Punkt Untergruppe	using pulse rate multipliers or dividers (G06F 7/70 takes precedence) [3]

Type			0001 17/11
numbers [3]  CODE 1772  2-Punkt Untergruppe Selecting or encoding within a word the position of one or more bits having a specified value, e.g. most or least significant one or sero detection, pronty encoders [2008.01]  3-Punkt Untergruppe Amangements for recorrenging, permuting or selecting data according to predetermined rules. Independently of the content of the data (according to the data (accor	Symbol	Тур	Titel
3-Punkt Untergruppe   Selecting or encoding within a word the position of one or more bits having a specified value, e.g. most or least significant one or zero detection, priority encoders [2006.01]	G06F 7/70	2-Punkt Untergruppe	
least significant one or zero detection, prontly encoders [2006.01]	G06F 7/72	2-Punkt Untergruppe	using residue arithmetic [3]
independently of the content of the data (according to the content of the data (606F 7/06, 606F 7/02; parallel/series conversion or vice versa. H03M 9/00) [2006.01]  2-Punkt Untergruppe for changing the order of data flow, e.g. matrix transposition, UFO buffers; Overflow or underflow handling therefor [2006.01]  606F 9/00	G06F 7/74	1-Punkt Untergruppe	
Arrangements for programme control, e.g. control unit (programme control for peripheral devices 606F 19/10) [4]  Co6F 9/02	G06F 7/76	1-Punkt Untergruppe	independently of the content of the data (according to the content of the data G06F 7/06, G06F 7/22;
G06F 9/02 1-Punkt Untergruppe	G06F 7/78	2-Punkt Untergruppe	
1-Punkt Untergruppe using record carriers containing only programme instructions (G06F 9/06 takes precedence)  1-Punkt Untergruppe using stored programme, i.e. using internal store of processing equipment to receive and retain programme G06F 9/22 2-Punkt Untergruppe Micro-control or micro-programme arrangements [3] G06F 9/24 3-Punkt Untergruppe Loading of the micro-programme [3] G06F 9/26 3-Punkt Untergruppe Address formation of the next micro-instruction (G06F 9/28 takes precedence) [3] G06F 9/28 3-Punkt Untergruppe Enhancement of operational speed, e.g. by using several micro-control devices operating in parallel [3] G06F 9/30 2-Punkt Untergruppe Arrangements for executing machine-instructions, e.g. instruction decode (for executing micro-instructions G06F 9/302 3-Punkt Untergruppe Controlling the executing of arithmetic operations [5] G06F 9/302 3-Punkt Untergruppe Controlling the executing of logical operations [5] G06F 9/308 3-Punkt Untergruppe Controlling single bit operations (G06F 9/305 takes precedence) [5] G06F 9/312 3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5] G06F 9/313 3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5] G06F 9/314 3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3] G06F 9/34 3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5] G06F 9/35 4-Punkt Untergruppe Addressing 15] G06F 9/35 4-Punkt Untergruppe Indirect addressing [5] G06F 9/38 3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]	G06F 9/00	Hauptgruppe	Arrangements for programme control, e.g. control unit (programme control for peripheral devices G06F 13/10) [4]
1-Punkt Untergruppe . using stored programme, i.e. using internal store of processing equipment to receive and retain programme (3)	G06F 9/02	1-Punkt Untergruppe	. using wired connections, e.g. plugboard
2-Punkt Untergruppe Micro-control or micro-programme arrangements [3]  3-Punkt Untergruppe Loading of the micro-programme [3]  3-Punkt Untergruppe Address formation of the next micro-instruction (G06F 9/28 takes precedence) [3]  3-Punkt Untergruppe Enhancement of operational speed, e.g. by using several micro-control devices operating in parallel [3]  3-Punkt Untergruppe Arrangements for executing machine-instructions, e.g. instruction decode (for executing micro-instructions G06F 9/302 Arrangements for executing subprogrammes G06F 9/40) [3]  3-Punkt Untergruppe Controlling the executing of arithmetic operations [5]  3-Punkt Untergruppe Controlling the executing of logical operations [5]  3-Punkt Untergruppe Controlling single bit operations (G06F 9/305 takes precedence) [5]  3-Punkt Untergruppe Controlling loading, storing or clearing operations [5]  3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3-Punkt Untergruppe With operation extension or modification [5]  3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence: subprogramme jump G06F 9/42) [3]  3-Punkt Untergruppe Address formation of the next instruction operand or the result (address translation G06F 12/00) [3, 5]  4-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  4-Punkt Untergruppe Indirect addressing [5]  4-Punkt Untergruppe Indirect addressing [5]  4-Punkt Untergruppe Indirect addressing [5]  506F 9/38  3-Punkt Untergruppe Indexed addressing [5]  506F 9/38  3-Punkt Untergruppe Indexed addressing [5]  506F 9/38  3-Punkt Untergruppe Indexed addressing [5]	G06F 9/04	1-Punkt Untergruppe	. using record carriers containing only programme instructions (G06F 9/06 takes precedence)
3-Punkt Untergruppe Loading of the micro-programme [3]  3-Punkt Untergruppe Address formation of the next micro-instruction (G06F 9/28 takes precedence) [3]  3-Punkt Untergruppe Enhancement of operational speed, e.g., by using several micro-control devices operating in parallel [3]  3-Punkt Untergruppe Arrangements for executing machine-instructions, e.g., instruction decode (for executing micro-instructions G06F 9/302 3-Punkt Untergruppe Controlling the executing of arithmetic operations [5]  3-Punkt Untergruppe Controlling the executing of logical operations [5]  3-Punkt Untergruppe Controlling single bit operations (G06F 9/305 takes precedence) [5]  3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3-Punkt Untergruppe With operation extension or modification [5]  3-Punkt Untergruppe Address formation of the next instruction, e.g., incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  606F 9/34 3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  606F 9/35 4-Punkt Untergruppe Indirect addressing [5]  606F 9/35 4-Punkt Untergruppe Indirect addressing [5]  606F 9/38 3-Punkt Untergruppe Indirect addressing [5]  606F 9/38 3-Punkt Untergruppe Indexed addressing [5]  606F 9/38 3-Punkt Untergruppe Indexed addressing [5]  606F 9/38 3-Punkt Untergruppe Indexed addressing [5]	G06F 9/06	1-Punkt Untergruppe	. using stored programme, i.e. using internal store of processing equipment to receive and retain programme
3.Punkt Untergruppe Address formation of the next micro-instruction (G06F 9/28 takes precedence) [3]  3.Punkt Untergruppe Enhancement of operational speed, e.g. by using several micro-control devices operating in parallel [3]  3.Punkt Untergruppe Arrangements for executing machine-instructions, e.g. instruction decode (for executing micro-instructions G06F 9/302 3.Punkt Untergruppe Controlling the executing of arithmetic operations [5]  3.Punkt Untergruppe Controlling the executing of logical operations [5]  3.Punkt Untergruppe Controlling single bit operations (G06F 9/305 takes precedence) [5]  3.Punkt Untergruppe Controlling loading, storing or clearing operations [5]  3.Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3.Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3.Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  3.Punkt Untergruppe Addressing or accessing the instruction operation the result (address translation G06F 12/00) [3, 5]  4.Punkt Untergruppe of multiple operands or results [5]  4.Punkt Untergruppe Indirect addressing [5]  4.Punkt Untergruppe Indirect addressing [5]  4.Punkt Untergruppe Indirect addressing [5]  4.Punkt Untergruppe Indexed addressing [5]  5.Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  5.Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/22	2-Punkt Untergruppe	Micro-control or micro-programme arrangements [3]
3-Punkt Untergruppe Enhancement of operational speed, e.g. by using several micro-control devices operating in parallel [3]  2-Punkt Untergruppe Arrangements for executing machine- instructions, e.g. instruction decode (for executing micro-instructions G06F 9/302 3-Punkt Untergruppe Controlling the executing of arithmetic operations [5]  3-Punkt Untergruppe Controlling the executing of logical operations [5]  3-Punkt Untergruppe Controlling single bit operations (G06F 9/305 takes precedence) [5]  3-Punkt Untergruppe Controlling loading, storing or clearing operations [5]  3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3-Punkt Untergruppe With operation extension or modification [5]  3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  4-Punkt Untergruppe Indirect addressing [5]  4-Punkt Untergruppe Indirect addressing [5]  3-Punkt Untergruppe Indexed addressing [5]  3-Punkt Untergruppe Indexed addressing [5]  3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]	G06F 9/24	3-Punkt Untergruppe	Loading of the micro-programme [3]
2-Punkt Untergruppe Arrangements for executing machine- instructions, e.g., instruction decode (for executing micro-instructions G06F 9/302 3-Punkt Untergruppe Controlling the executing of arithmetic operations [5]  3-Punkt Untergruppe Controlling the executing of logical operations [5]  3-Punkt Untergruppe Controlling single bit operations (G06F 9/305 takes precedence) [5]  3-Punkt Untergruppe Controlling loading, storing or clearing operations [5]  3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3-Punkt Untergruppe With operation extension or modification [5]  3-Punkt Untergruppe With operation extension or modification [5]  3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  4-Punkt Untergruppe Indirect addressing [5]  4-Punkt Untergruppe Indirect addressing [5]  3-Punkt Untergruppe Indexed addressing [5]	G06F 9/26	3-Punkt Untergruppe	Address formation of the next micro-instruction (G06F 9/28 takes precedence) [3]
G06F 9/302 3-Punkt Untergruppe Controlling the executing of arithmetic operations [5] G06F 9/305 3-Punkt Untergruppe Controlling the executing of logical operations [5] G06F 9/308 3-Punkt Untergruppe Controlling single bit operations (G06F 9/305 takes precedence) [5] G06F 9/312 3-Punkt Untergruppe Controlling loading, storing or clearing operations [5] G06F 9/315 3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5] G06F 9/318 3-Punkt Untergruppe with operation extension or modification [5] G06F 9/32 3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3] G06F 9/34 3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5] G06F 9/34 4-Punkt Untergruppe of multiple operands or results [5] G06F 9/35 4-Punkt Untergruppe Indirect addressing [5] G06F 9/36 3-Punkt Untergruppe Indexed addressing [5] G06F 9/37 4-Punkt Untergruppe Indexed addressing [5] G06F 9/38 3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3] G06F 9/40 2-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/28	3-Punkt Untergruppe	Enhancement of operational speed, e.g. by using several micro-control devices operating in parallel [3]
3-Punkt Untergruppe Controlling the executing of logical operations [5]  G06F 9/308 3-Punkt Untergruppe Controlling single bit operations (G06F 9/305 takes precedence) [5]  G06F 9/312 3-Punkt Untergruppe Controlling loading, storing or clearing operations [5]  G06F 9/315 3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  G06F 9/318 3-Punkt Untergruppe with operation extension or modification [5]  G06F 9/32 3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  G06F 9/34 3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  G06F 9/35 4-Punkt Untergruppe of multiple operands or results [5]  G06F 9/35 4-Punkt Untergruppe Indirect addressing [5]  G06F 9/38 3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  G06F 9/40 2-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/30	2-Punkt Untergruppe	
3-Punkt Untergruppe Controlling single bit operations (G06F 9/305 takes precedence) [5]  3-Punkt Untergruppe Controlling loading, storing or clearing operations [5]  3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3-Punkt Untergruppe with operation extension or modification [5]  3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  G06F 9/34 3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  G06F 9/35 4-Punkt Untergruppe Indirect addressing [5]  G06F 9/355 4-Punkt Untergruppe Indexed addressing [5]  G06F 9/38 3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  G06F 9/40 2-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/302	3-Punkt Untergruppe	Controlling the executing of arithmetic operations [5]
3-Punkt Untergruppe Controlling loading, storing or clearing operations [5]  3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3-Punkt Untergruppe with operation extension or modification [5]  3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  4-Punkt Untergruppe of multiple operands or results [5]  4-Punkt Untergruppe Indirect addressing [5]  4-Punkt Untergruppe Indexed addressing [5]  3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  3-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/305	3-Punkt Untergruppe	Controlling the executing of logical operations [5]
3-Punkt Untergruppe Controlling moving, shifting or rotation operations [5]  3-Punkt Untergruppe with operation extension or modification [5]  3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  4-Punkt Untergruppe of multiple operands or results [5]  4-Punkt Untergruppe Indirect addressing [5]  4-Punkt Untergruppe Indexed addressing [5]  3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  3-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/308	3-Punkt Untergruppe	Controlling single bit operations (G06F 9/305 takes precedence) [5]
3-Punkt Untergruppe with operation extension or modification [5]  3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  4-Punkt Untergruppe of multiple operands or results [5]  4-Punkt Untergruppe Indirect addressing [5]  4-Punkt Untergruppe Indexed addressing [5]  5-Punkt Untergruppe Indexed addressing [5]  5-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  5-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/312	3-Punkt Untergruppe	Controlling loading, storing or clearing operations [5]
3-Punkt Untergruppe Address formation of the next instruction, e.g. incrementing the instruction counter, jump (G06F 9/38 takes precedence; subprogramme jump G06F 9/42) [3]  3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  4-Punkt Untergruppe of multiple operands or results [5]  4-Punkt Untergruppe Indirect addressing [5]  4-Punkt Untergruppe Indexed addressing [5]  3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  G06F 9/40 2-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/315	3-Punkt Untergruppe	Controlling moving, shifting or rotation operations [5]
takes precedence; subprogramme jump G06F 9/42) [3]  G06F 9/34  3-Punkt Untergruppe Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]  G06F 9/345  4-Punkt Untergruppe of multiple operands or results [5]  G06F 9/35  4-Punkt Untergruppe Indirect addressing [5]  G06F 9/38  3-Punkt Untergruppe Indexed addressing [5]  G06F 9/38  3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  G06F 9/40  2-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/318	3-Punkt Untergruppe	with operation extension or modification [5]
4-Punkt Untergruppe of multiple operands or results [5]  4-Punkt Untergruppe Indirect addressing [5]  4-Punkt Untergruppe Indexed addressing [5]  4-Punkt Untergruppe Indexed addressing [5]  5-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  5-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/32	3-Punkt Untergruppe	
G06F 9/35 4-Punkt Untergruppe Indirect addressing [5] G06F 9/355 4-Punkt Untergruppe Indexed addressing [5] G06F 9/38 3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3] G06F 9/40 2-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/34	3-Punkt Untergruppe	Addressing or accessing the instruction operand or the result (address translation G06F 12/00) [3, 5]
G06F 9/355  4-Punkt Untergruppe Indexed addressing [5]  G06F 9/38  3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  G06F 9/40  2-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/345	4-Punkt Untergruppe	of multiple operands or results [5]
G06F 9/38  3-Punkt Untergruppe Concurrent instruction execution, e.g. pipeline, look ahead [3]  G06F 9/40  2-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/35	4-Punkt Untergruppe	Indirect addressing [5]
G06F 9/40 2-Punkt Untergruppe Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]	G06F 9/355	4-Punkt Untergruppe	Indexed addressing [5]
	G06F 9/38	3-Punkt Untergruppe	Concurrent instruction execution, e.g. pipeline, look ahead [3]
G06F 9/42 3-Punkt Untergruppe Formation of subprogramme-jump address or of return address [3]	G06F 9/40	2-Punkt Untergruppe	Arrangements for executing subprogrammes, i.e. combinations of several instructions [3]
	G06F 9/42	3-Punkt Untergruppe	Formation of subprogramme-jump address or of return address [3]

Symbol	Тур	Titel
G06F 9/44	2-Punkt Untergruppe	Arrangements for executing specific programmes [3]
G06F 9/445	3-Punkt Untergruppe	Programme loading or initiating [5]
G06F 9/45	3-Punkt Untergruppe	Compilation or interpretation of high level programme languages [5]
G06F 9/455	3-Punkt Untergruppe	Emulation; Software simulation [5]
G06F 9/46	2-Punkt Untergruppe	Multiprogramming arrangements [3]
G06F 9/48	3-Punkt Untergruppe	Programme initiating; Programme switching, e.g. by interrupt [7]
G06F 9/50	3-Punkt Untergruppe	Allocation of resources, e.g. of the central processing unit (CPU) [7]
G06F 9/52	3-Punkt Untergruppe	Programme synchronisation; Mutual exclusion, e.g. by means of semaphores [7]
G06F 9/54	3-Punkt Untergruppe	Interprogramme communication [7]
G06F 11/00	Hauptgruppe	Error detection; Error correction; Monitoring (methods or arrangements for verifying the correctness of marking on a record carrier G06K 5/00; in information storage based on relative movement between record carrier and transducer G11B, e.g. G11B 20/18; in static stores G11C 29/00; coding, decoding or code conversion, for error detection or error correction, in general H03M 13/00) [4]
G06F 11/07	1-Punkt Untergruppe	. Responding to the occurrence of a fault, e.g. fault tolerance [7]
G06F 11/08	2-Punkt Untergruppe	Error detection or correction by redundancy in data representation, e.g. by using checking codes
G06F 11/10	3-Punkt Untergruppe	Adding special bits or symbols to the coded information, e.g. parity check, casting out nines or elevens
G06F 11/14	2-Punkt Untergruppe	Error detection or correction of the data by redundancy in operation, e.g. by using different operation sequences leading to the same result (G06F 11/16 takes precedence) [3]
G06F 11/16	2-Punkt Untergruppe	Error detection or correction of the data by redundancy in hardware [3]
G06F 11/18	3-Punkt Untergruppe	using passive fault-masking of the redundant circuits, e.g. by quadding or by majority decision circuits [3]
G06F 11/20	3-Punkt Untergruppe	using active fault-masking, e.g. by switching out faulty elements or by switching in spare elements [3]
G06F 11/22	1-Punkt Untergruppe	. Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing (testing of digital circuits, e.g. of separate computer components, G01R 31/317) [3]
G06F 11/24	2-Punkt Untergruppe	Marginal testing [3]
G06F 11/25	2-Punkt Untergruppe	Testing of logic operation, e.g. by logic analysers [6]
G06F 11/26	2-Punkt Untergruppe	Functional testing [3]
G06F 11/263	3-Punkt Untergruppe	Generation of test inputs, e.g. test vectors, patterns or sequences [6]
G06F 11/267	3-Punkt Untergruppe	Reconfiguring circuits for testing, e.g. LSSD, partitioning [6]
G06F 11/27	3-Punkt Untergruppe	Built-in tests [6]
G06F 11/273	3-Punkt Untergruppe	Tester hardware, i.e. output processing circuits [6]
G06F 11/277	4-Punkt Untergruppe	with comparison between actual response and known fault-free response [6]
G06F 11/28	1-Punkt Untergruppe	. by checking the correct order of processing (G06F 11/07, G06F 11/22 take precedence; monitoring patterns of pulse trains H03K 5/19) [3]
G06F 11/30	1-Punkt Untergruppe	. Monitoring [3]

Symbol	Тур	Titel
G06F 11/32	2-Punkt Untergruppe	with visual indication of the functioning of the machine [3]
G06F 11/34	2-Punkt Untergruppe	Recording or statistical evaluation of computer activity, e.g. of down time, of input/output operation [3]
G06F 11/36	1-Punkt Untergruppe	. Preventing errors by testing or debugging of software [7]
G06F 12/00	Hauptgruppe	Accessing, addressing or allocating within memory systems or architectures (information storage
		in general G11) [4, 5]
G06F 12/02	1-Punkt Untergruppe	. Addressing or allocation; Relocation (programme address sequencing G06F 9/00; arrangements for selecting an address in a digital store G11C 8/00) [4]
G06F 12/04	2-Punkt Untergruppe	Addressing variable-length words or parts of words [4]
G06F 12/06	2-Punkt Untergruppe	Addressing a physical block of locations, e.g. base addressing, module addressing, address space extension, memory dedication (G06F 12/08 takes precedence) [4]
G06F 12/08	2-Punkt Untergruppe	in hierarchically structured memory systems, e.g. virtual memory systems [4]
G06F 12/10	3-Punkt Untergruppe	Address translation [4]
G06F 12/12	3-Punkt Untergruppe	Replacement control [4]
G06F 12/14	1-Punkt Untergruppe	. Protection against unauthorised use of memory [4]
G06F 12/16	1-Punkt Untergruppe	. Protection against loss of memory contents [4]
G06F 13/00	Hauptgruppe	Interconnection of, or transfer of information or other signals between, memories, input/output devices or central processing units (interface circuits for specific input/output devices G06F 3/00; multi-processor systems G06F 15/16; transmission of digital information in general H04L; selecting H04Q) [4]
G06F 13/10	1-Punkt Untergruppe	. Programme control for peripheral devices (G06F 13/14-G06F 13/42 take precedence) [4]
G06F 13/12	2-Punkt Untergruppe	using hardware independent of the central processor, e.g. channel or peripheral processor [4]
G06F 13/14	1-Punkt Untergruppe	. Handling requests for interconnection or transfer [4]
G06F 13/16	2-Punkt Untergruppe	for access to memory bus (G06F 13/28 takes precedence) [4]
G06F 13/18	3-Punkt Untergruppe	with priority control [4]
G06F 13/20	2-Punkt Untergruppe	for access to input/output bus [4]
G06F 13/22	3-Punkt Untergruppe	using successive scanning, e.g. polling (G06F 13/24 takes precedence) [4]
G06F 13/24	3-Punkt Untergruppe	using interrupt (G06F 13/32 takes precedence) [4]
G06F 13/26	4-Punkt Untergruppe	with priority control [4]
G06F 13/28	3-Punkt Untergruppe	using burst mode transfer, e.g. direct memory access, cycle steal (G06F 13/32 takes precedence) [4]
G06F 13/30	4-Punkt Untergruppe	with priority control [4]
G06F 13/32	3-Punkt Untergruppe	using combination of interrupt and burst mode transfer [4]
G06F 13/34	4-Punkt Untergruppe	with priority control [4]
G06F 13/36	2-Punkt Untergruppe	for access to common bus or bus system [4]
G06F 13/362	3-Punkt Untergruppe	with centralised access control [5]
G06F 13/364	4-Punkt Untergruppe	using independent requests or grants, e.g. using separated request and grant lines [5]
G06F 13/366	4-Punkt Untergruppe	using a centralised polling arbiter [5]

Symbol	Тур	Titel
G06F 13/368	3-Punkt Untergruppe	with decentralised access control [5]
G06F 13/37	4-Punkt Untergruppe	using a physical-position-dependent priority, e.g. daisy chain, round robin or token passing [5]
G06F 13/372	4-Punkt Untergruppe	using a time-dependent priority, e.g. individually loaded time counters or time slot [5]
G06F 13/374	4-Punkt Untergruppe	using a self-select method with individual priority code comparator [5]
G06F 13/376	4-Punkt Untergruppe	using a contention resolving method, e.g. collision detection, collision avoidance [5]
G06F 13/378	4-Punkt Untergruppe	using a parallel poll method [5]
G06F 13/38	1-Punkt Untergruppe	. Information transfer, e.g. on bus (G06F 13/14 takes precedence) [4]
G06F 13/40	2-Punkt Untergruppe	Bus structure [4]
G06F 13/42	2-Punkt Untergruppe	Bus transfer protocol, e.g. handshake; Synchronisation (synchronisation in transmission of digital information in general H04L 7/00) [4]
G06F 15/00	Hauptgruppe	Digital computers in general (details G06F $1/00$ -G06F $13/00$ ); Data processing equipment in general (neural networks for image data processing G06T)
G06F 15/02	1-Punkt Untergruppe	. manually operated with input through keyboard and computation using a built-in programme, e.g. pocket calculators
G06F 15/04	1-Punkt Untergruppe	. programmed simultaneously with the introduction of data to be processed, e.g. on the same record carrier
G06F 15/08	1-Punkt Untergruppe	. using a plugboard for programming [5]
G06F 15/10	2-Punkt Untergruppe	Tabulators [5]
G06F 15/12	3-Punkt Untergruppe	having provision for both printed and punched output [5]
G06F 15/14	2-Punkt Untergruppe	Calculating-punches [5]
G06F 15/16	1-Punkt Untergruppe	. Combinations of two or more digital computers each having at least an arithmetic unit, a programme unit and a register, e.g. for a simultaneous processing of several programmes (interface circuits for specific input/output devices G06F 3/00; multi-programming arrangements G06F 9/46; transmission of digital information in general H04L, e.g. in computer networks H04L 12/00; selecting H04Q)
G06F 15/163	2-Punkt Untergruppe	Interprocessor communication [6]
G06F 15/167	3-Punkt Untergruppe	using a common memory, e.g. mailbox (memory protection G06F 12/14; memory access priority G06F 13/18) [6]
G06F 15/17	3-Punkt Untergruppe	using an input/output type connection, e.g. channel, I/O port [6]
G06F 15/173	3-Punkt Untergruppe	using an interconnection network, e.g. matrix, shuffle, pyramid, star, snowflake (interface switching circuits G06F 13/40) [6]
G06F 15/177	2-Punkt Untergruppe	Initialisation or configuration control (configuration control for monitoring, testing or in case of failure G06F 11/00) [6]
G06F 15/18	1-Punkt Untergruppe	. in which a programme is changed according to experience gained by the computer itself during a complete run; Learning machines (adaptive control systems G05B 13/00)
G06F 15/76	1-Punkt Untergruppe	. Architectures of general purpose stored programme computers (with programme plugboard G06F 15/08; multicomputers G06F 15/16; general purpose image data processing G06T 1/00) [5, 6]
G06F 15/78	2-Punkt Untergruppe	comprising a single central processing unit [5]
G06F 15/80	2-Punkt Untergruppe	comprising an array of processing units with common control, e.g. single instruction multiple data processors (G06F 15/82 takes precedence) [5]

Symbol	Тур	Titel
G06F 15/82	2-Punkt Untergruppe	data or demand driven [5]
G06F 17/00	Hauptgruppe	Digital computing or data processing equipment or methods, specially adapted for specific functions [6]
G06F 17/10	1-Punkt Untergruppe	. Complex mathematical operations [6]
G06F 17/11	2-Punkt Untergruppe	for solving equations [6]
G06F 17/12	3-Punkt Untergruppe	Simultaneous equations [6]
G06F 17/13	3-Punkt Untergruppe	Differential equations (using digital differential analysers G06F 7/64) [6]
G06F 17/14	2-Punkt Untergruppe	Fourier, Walsh or analogous domain transformations [6]
G06F 17/15	2-Punkt Untergruppe	Correlation function computation [6]
G06F 17/16	2-Punkt Untergruppe	Matrix or vector computation [6]
G06F 17/17	2-Punkt Untergruppe	Function evaluation by approximation methods, e.g. inter- or extrapolation, smoothing, least mean square method (interpolation for numerical control G05B 19/18) [6]
G06F 17/18	2-Punkt Untergruppe	for evaluating statistical data [6]
G06F 17/20	1-Punkt Untergruppe	. Handling natural language data (speech analysis or synthesis G10L) [6]
G06F 17/21	2-Punkt Untergruppe	Text processing (G06F 17/27, G06F 17/28 take precedence; systems for composing machines B41B 27/00) [6]
G06F 17/22	3-Punkt Untergruppe	Manipulating or registering by use of codes, e.g. in sequence of text characters [6]
G06F 17/24	3-Punkt Untergruppe	Editing, e.g. insert/delete [6]
G06F 17/25	3-Punkt Untergruppe	Automatic justification [6]
G06F 17/26	3-Punkt Untergruppe	Automatic hyphenation [6]
G06F 17/27	2-Punkt Untergruppe	Automatic analysis, e.g. parsing, orthograph correction [6]
G06F 17/28	2-Punkt Untergruppe	Processing or translating of natural language (G06F 17/27 takes precedence) [6]
G06F 17/30	1-Punkt Untergruppe	. Information retrieval; Database structures therefor [6]
G06F 17/40	1-Punkt Untergruppe	. Data acquisition and logging (for input to computer G06F 3/00) [6]
G06F 17/50	1-Punkt Untergruppe	. Computer-aided design (for the design of test circuits for static stores G11C 29/54) [6, 2006.01]
G06F 17/60	Gelöscht	(transferred to G06Q)
G06F 19/00	Hauptgruppe	Digital computing or data processing equipment or methods, specially adapted for specific applications (G06F 17/00 takes precedence; data processing systems or methods specially adapted for administrative, commercial, financial, managerial, supervisory or forecasting purposes G06Q) [6, 2006.01]
G06F 21/00	Hauptgruppe	Security arrangements for protecting computers or computer systems against unauthorised activity (multiprogramming G06F 9/46; protection against unauthorised use of memory G06F 12/14; dispensing apparatus actuated by coded identity card or credit card G07F 7/08; equipment anti-theft monitoring by a central station G08B 26/00; secret or secure communication H04L 9/00; data switching networks H04L 12/00) [2006.01]
G06F 21/02	1-Punkt Untergruppe	. by protecting specific internal components of computers [2006.01]
G06F 21/04	1-Punkt Untergruppe	. by protecting specific peripheral devices, e.g. keyboards or displays [2006.01]
G06F 21/06	1-Punkt Untergruppe	. by sensing unauthorised manipulation of, or intrusion into, an enclosure e.g. a housing or a room [2006.01]

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G06F 17/21

Symbol	Тур	Titel
G06F 21/20	1-Punkt Untergruppe	. by restricting access to nodes in a computer system or computer network [2006.01]
G06F 21/22	1-Punkt Untergruppe	. by restricting access to, or manipulation of, programmes or processes [2006.01]
G06F 21/24	1-Punkt Untergruppe	. by protecting data directly, e.g. by labelling [2006.01]